

COMPARSION OF SRAM DESIGN USING VLSI

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With the emerging use of portable consumer electronics, power consumption has become an important performance characteristic for a chip due to both limited battery life in portable systems and also due to expensive packages and heat sinks required by high power levels. In this project is about designing and comparing a multibank memory with multiport memory cell versus the conventionally used multiport and multibank memory design techniques. By analyzing different arrangements of these memories and answer can be determined has to whether or not the use of multibank memory with multiport memory cell is better than multiport and multibank memories for some memory sizes.

