

## **DETECTION OF SOFT ERRORS IN FFT USING CONCURRENT ERROR DETECTION TECHNIQUES**

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Detection of errors is a major task in FFT. FFT is widely used in signal processing applications due to their high processing speed and energy efficiency. In this paper we implement some concurrent input and check scheme techniques and compare the error detection rate and resources with Parseval Sum of Square techniques which is the common technique used to detect errors in FFT.

Keywords—FFT (Fast Fourier Transformer), CICS (Concurrent Input and Check Scheme), DMR (Dual Modular Redundancy), PSoS (Parseval Sum of Square).